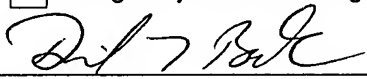
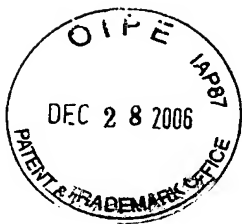




JFW

AMENDMENT TRANSMITTAL LETTER				Docket No. R2184.0247/P247	
Application No. 10/633,681-Conf. #6878		Filing Date August 5, 2003		Examiner E. J. Wojciechowicz	
				Art Unit 2815	
Applicant(s): Keiichi Yoshioka					
Invention: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND FABRICATION METHOD THEREOF					
TO THE COMMISSIONER FOR PATENTS					
Transmitted herewith is an amendment in the above-identified application.					
The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	17	- 20 =	0	x	0.00
Independent Claims	4	- 7 =	0	x	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00
<input checked="" type="checkbox"/> Large Entity <input type="checkbox"/> Small Entity					
<input checked="" type="checkbox"/> No additional fee is required for this amendment.					
<input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of \$ _____. A duplicate copy of this sheet is enclosed.					
<input type="checkbox"/> A check in the amount of \$ _____ to cover the filing fee is enclosed.					
<input type="checkbox"/> Payment by credit card. Form PTO-2038 is attached.					
<input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. <u>04-1073</u> as described below. A duplicate copy of this sheet is enclosed.					
<input checked="" type="checkbox"/> Credit any overpayment.					
<input checked="" type="checkbox"/> Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.					
 David T. Beck Attorney/Agent Reg. No.: 54,985				Dated: <u>December 28, 2006</u>	
DICKSTEIN SHAPIRO LLP 1825 Eye Street, NW Washington, DC 20006-5403 (202) 420-4742					



Docket No.: R2184.0247/P247
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Keiichi Yoshioka

Application No.: 10/633,681

Confirmation No.: 6878

Filed: August 5, 2003

Art Unit: 2815

For: SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE AND FABRICATION
METHOD THEREOF

Examiner: E. J. Wojciechowicz

RESPONSE TO NON-FINAL OFFICE ACTION

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated October 10, 2006, please amend the above-identified U.S. patent application as follows:

A Listing of the Claims begins on page 2 of this paper.

Remarks begin on page 7 of this paper.